ABSTRACT OF THE DISCLOSURE

In a liquid crystal display device, in each unit pixel which is formed as a region surrounded by scanning signal lines and date signal lines includes pixel electrode to which signals of the date signal line is electrically supplied through a thin film transistor and common electrode which are electrically connected with the common signal line, and common signal electrode is arranged to be superposed on the common signal line by way of an insulation layer, and the pixel electrode is electrically connected with a source electrode of the thin film transistor via a through hole which penetrates the insulation layer, and the common electrode is formed such that the common electrode extend in the inside of the unit pixel while covering the common signal line.